Introduction: The EE 2390 laboratory is an integral part of the Digital Systems Design course. The student will be provided with an opportunity to develop design and debugging skills using contemporary SSI, LSI and VLSI (CPLD and FPGA) hardware as well as a contemporary industrial integrated software suite for design entry, simulation, and synthesis. The student should acknowledge that most, if not all, labs will not proceed smoothly from start to finish. It is expected that the student will invest a significant amount of time and critical thinking to develop the skills for using the tools available in lab, and a portion of this time will involve exorcising the gremlins in the software and hardware. The Teaching Assistant will act as an experienced user of the tools, however they are not expected to “solve all problems for all students.” Finally, the student should not consider the lab to be “guaranteed points.” In fact, the lab will be very challenging, and it is expected that not everyone will receive full marks for each lab exercise.

Attendance Policy: Attendance at each and every lab session, at your assigned lab time, is absolutely required of all students. Failure to satisfactorily complete just one lab exercise will result in failing the class (see ECE Department Policy, available at the Department Office). If for any reason you must miss an assigned lab time, you must obtain permission to do so from your Teaching Assistant before the time of your assigned lab meeting. An un-permitted absence which is not accompanied by a University Excused Absence Slip (available from the Office of Student Life) will result in a grade of F in the class.

Grading Per Lab Exercise (except final project):

<table>
<thead>
<tr>
<th>Component</th>
<th>Points</th>
</tr>
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<tbody>
<tr>
<td>Prelab</td>
<td>10</td>
</tr>
<tr>
<td>Lab Notebook</td>
<td>2</td>
</tr>
<tr>
<td>Procedure</td>
<td>8</td>
</tr>
<tr>
<td>Quiz</td>
<td>10</td>
</tr>
<tr>
<td>Total</td>
<td>30</td>
</tr>
</tbody>
</table>

Satisfactory completion of each lab exercise will be based upon receiving a grade of at least 15 out of the possible 30 points. Re-doing a component of a lab exercise is the responsibility of the student, arranged with the permission of their Teaching Assistant. Note: The final project will be worth a total of 60 points, no quiz.

Bring to Lab: One of the objectives of the lab is to develop documentation and communication skills typical of a professional digital design research and development laboratory. Toward this end, each student is required to obtain, bring to lab, and utilize the following items:

2. An appropriate Lab Notebook (a bound Composition Notebook, either with college-ruled or square-grid pages, is an inexpensive and satisfactory solution). See the Course Syllabus and Introduction section of the Laboratory Manual for more detail.
3. A sturdy folder or binder for any loose-leaf papers. This should be labeled and utilized for only EE 2390.
4. Your own Digilent Basys 2 Spartan-3E FPGA Board, available on-line at a student price of $49.00 at this ULR: http://www.digilentinc.com/Products/Detail.cfm?Prod=BASYS2

The first three items must be brought to each lab session, and the developing content of each should be as follows:

Laboratory Manual

Each Lab Exercise will be introduced via the instructions contained in the Laboratory Manual. The procedure steps suggested for each Lab Exercise need not be transcribed into the Lab Notebook, however, any significant variation from the suggested procedure should be described therein.

Lab Notebook

(a) Prelab: each lab exercise has an associated Prelab which must be completed by the student before lab time. Prelabs will be graded at the start of the lab session, and only Prelabs which are permanently contained (written, taped, glued) into the Lab Notebook will be accepted for grading. Looseleaf Prelabs will receive a grade of zero. Schematics should be neatly drawn (use a straight-edge if drawing by hand) and all features clearly annotated. Your Teaching
Assistant has only one minute per student to grade the Prelab, so this work must be very carefully and cleanly entered into the notebook. The student will not have time at the beginning of the lab session to complete the Prelab as they will be taking a quiz at that time.

(b) Corrected Prelab: any corrections to the Prelab (computations, design changes, etc.) must be permanently entered into the Lab Notebook in a manner which retains the original Prelab while clearly indicating the correction.

(c) Schematics/HDL Code/Timing Diagrams: printouts of any schematics, pertinent “software modules” and graphical outputs associated with the design and simulation should be permanently contained in the Lab Notebook. To accomplish this task, you will have to investigate the sizing and presentation features of the printer output tools available with the lab software. Skill with a photocopier might come in handy as well.

(d) Annotations: appropriate hand written annotations should be provided on all computer generated outputs to aid in summarizing the information provided.

(e) Hardware Implementation Details: a summary of the results of any hardware implementation and testing should be stated clearly.

(f) Lessons Learned: each student should summarize, in writing, any new facts or skills learned in the completion of the lab.

Folder or Binder

(a) Lab Manual: at the student’s option, pertinent hardcopy pages of the Laboratory Manual may be attached or clipped inside the folder or binder.

(b) Graded Lab Quizzes: at the beginning of each lab session, the Teaching Assistant will administer a quiz based upon the principles of the lab exercise completed the previous week.

(c) Any Other Looseleaf Supplementary Resources: items should include this handout and other handouts provided in lecture or references discovered on the web.1 As these rise in value and frequency of use, they should probably be permanently entered into the Lab Notebook.

Lab Notebook Grading: The Teaching Assistant will examine the notebooks at the start of each lab session to award a cursory grade for format and completeness.

Computing Tools / Availability: The Xilinx software utilized in the laboratory is also available for student use in the open computer laboratory (EN 5038). Students are encouraged to work outside of scheduled lab time to gain familiarity and expertise with the software. One readily conceivable reward is that a full-point Prelab is one that has computed confirmation of its correctness. Note, however, that you must attend your regularly scheduled lab in order to have Prelabs graded, Quizzes administered, and Procedures (with hardware implementation, typically) confirmed by the Teaching Assistant.

Issues of Academic Dishonesty: Each student is expected to independently complete and submit their own Prelab efforts. Lab partners should share in the decision of “what to implement” in lab. Changes to the Prelab effort must be clearly indicated in the Lab Notebook in order to justify the simulation and hardware verification obtained in lab. Copying of another individual’s work (electronically or by other means) and representing it as one’s own efforts is considered to be academically dishonest and will be dealt with according to the guidelines of the University, pursuant to University Regulation 6-802.

Student/TA Info

Student Name and W Number: ____________________________________

Lab TA (Name and Email): _______________________________________

Lab Section: _________________________________________________

Lab Partner: ________________________________________________

1 You might consider browsing through the website provided by the manufacturer of our software tools. Lots of good stuff can be viewed at http://www.xilinx.com